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## PALM INTRANET

# Inventor Information for 10/759078

Inventor Name	City	State/Country
TUTTLE, MARK E.	BOISE	IDAHO

[Appln Info](#) [Contents](#) [Petition Info](#) [Atty/Agent Info](#) [Continuity Data](#) [Foreign Data](#) [Inventors](#)

Search Another: Application#   or Patent#

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US 20060099797 A1	20060511	Integrated circuits with contemporaneously formed array electrodes and logic interconnects	438/618	438/241; 438/258	Seyyedy; Mirmajid et al.
US 20060082445 A1	20060420	Radio frequency data communications device	340/10.4	340/10.1; 340/572.4; 340/825.36	O'Toole; James E. et al.
US 20060077704 A1	20060413	Integrated circuit including sensor to sense environmental data, and system for testing	365/158		Tuttle; Mark E. et al.
US 20060019422 A1	20060126	Magnetic shield for integrated circuit packaging	438/55		Tuttle; Mark E. et al.
US 20050270830 A1	20051208	Integrated circuit structure formed by damascene process	365/158	257/E43.006; 365/173	Tuttle, Mark E.
US 20050231626 A1	20051020	Packaged microelectronic imagers and methods of packaging microelectronic imagers	348/340	257/E31.117; 257/E31.128	Tuttle, Mark E. et al.
US 20050190620 A1	20050901	Magnetic tunneling junction antifuse device	365/202		Seyyedy, Mirmajid et al.
US 20050133118 A1	20050623	Single substrate annealing of magnetoresistive structure	148/108	148/121	Tuttle, Mark E. et al.
US 20050110889 A1	20050526	Packaged microelectronic imagers and methods of packaging microelectronic imagers	348/340	257/E31.117; 257/E31.128; 348/294	Tuttle, Mark E. et al.
US 20050088314 A1	20050428	Radio frequency data communications device	340/825.36		O'Toole, James E. et al.
US 20050051818 A1	20050310	Integrated circuit structure formed by damascene process	257/295	257/E43.006	Tuttle, Mark E.
US 20040213043	20041028	Integrated circuit including sensor to	365/173		Tuttle, Mark E. et al.

A1		sense environmental data, method of compensating an MRAM integrated circuit for the effects of an external magnetic field, MRAM integrated circuit, and method of testing			al.
US 20040201919 A1	20041014	Magnetoresistive memory and method of manufacturing the same	360/126	257/421; 29/603.1; 365/173; 438/3	Tuttle, Mark E.
US 20040201457 A1	20041014	Radio frequency data communications device	340/10.33		O'Toole, James E. et al.
US 20040188799 A1	20040930	Magnetic tunneling junction antifuse device	257/530		Seyyedy, Mirmajid et al.
US 20040160795 A1	20040819	Integrated circuits with contemporaneously formed array electrodes and logic interconnects	365/63	365/51	Seyyedy, Mirmajid et al.
US 20040145453 A1	20040729	Wireless identification device, RFID device with push-on/push off switch, and method of manufacturing wireless identification device	340/10.1	235/491	Tuttle, Mark E.
US 20040144997 A1	20040729	Control of MTJ tunnel area	257/200		Tuttle, Mark E.
US 20040119095 A1	20040624	Magnetic shield for integrated circuit packaging	257/200	257/E23.114; 257/E23.124	Tuttle, Mark E. et al.
US 20040106271 A1	20040603	Processing method of forming MRAM circuitry	438/484	257/E21.218; 257/E21.252; 257/E21.577	Li, Li et al.
US 20040085190 A1	20040506	Radio frequency data communications device with adjustable receiver sensitivity and method	340/10.1		Tuttle, Mark E. et al.

US 20040042261 A1	20040304	Combination etch stop and in situ resistor in a magnetoresistive memory and methods for fabricating same	365/158	257/E21.665	Tuttle, Mark E.
US 20040041184 A1	20040304	MRAM sense layer area control	257/295		Tuttle, Mark E. et al.
US 20040041182 A1	20040304	MRAM sense layer area control	257/295		Tuttle, Mark E. et al.
US 20040040628 A1	20040304	Single substrate annealing of magnetoresistive structure	148/108		Tuttle, Mark E. et al.
US 20040029296 A1	20040212	Magnetoresistive memory and method of manufacturing the same	438/3		Tuttle, Mark E.
US 20030199167 A1	20031023	Control of MTJ tunnel area	438/692		Tuttle, Mark E.
US 20030179601 A1	20030925	Magnetic tunneling junction antifuse device	365/158		Seyyedy, Mirmajid et al.
US 20030134513 A1	20030717	Methods of forming integrated circuitry, semiconductor processing methods, and processing method of forming MRAM circuitry	438/694	257/E21.218; 257/E21.252; 257/E21.577	Li, Li et al.
US 20030132494 A1	20030717	Magnetic shield for integrated circuit packaging	257/433	257/E23.114; 257/E23.124	Tuttle, Mark E. et al.
US 20030128119 A1	20030710	Electrical apparatuses, methods of forming electrical apparatuses, and termite sensing methods	340/572.8	340/573.2	Lake, Rickie C. et al.
US 20030117866 A1	20030626	Recessed magnetic storage element and method of formation	365/200		Tuttle, Mark E.
US 20030099210 A1	20030529	RADIO FREQUENCY DATA COMMUNICATIONS	370/311		O'TOOLE, JAMES E. et al.

		DEVICE			
US 20030080853 A1	20030501	Radio frequency data communications device with adjustable receiver sensitivity and method	340/10.1		Tuttle, Mark E. et al.
US 20030043949 A1	20030306	Radio frequency data communications device	375/374		O'Toole, James E. et al.
US 20020186583 A1	20021212	Recessed magnetic storage element and method of formation	365/171	365/173	Tuttle, Mark E.
US 20020174153 A1	20021121	Radio frequency data communications device	708/252	340/10.1; 370/311	O'Toole, James E. et al.
US 20020140546 A1	20021003	Wireless identification device, RFID device with push-on/push off switch, and method of manufacturing wireless identification device	340/10.1	340/572.1	Tuttle, Mark E.
US 20020075184 A1	20020620	Communication devices, remote intelligent communication devices, electronic communication devices, methods of forming remote intelligent communication devices and methods of forming a radio frequency identification device	343/700MS	343/846	Tuttle, Mark E.
US 20020058404 A1	20020516	Methods of forming electrically conductive interconnections and electrically interconnected substrates	438/618	257/E21.508; 257/E21.511	Medlen, Curtis M et al.
US 20010050580 A1	20011213	Radio frequency data communications device	327/158		O'Toole, James E. et al.
US	20011122	Wireless identification	340/10.4	341/34	Tuttle,

20010043141 A1		device, rfid device, and method of manufacturing wireless identification device			Mark E.
US 20010007335 A1	20010712	Method of manufacturing an enclosed transceiver	235/492		Tuttle, Mark E. et al.
US 20010004237 A1	20010621	Termite sensing methods	340/573.1	340/693.5; 43/132.1	Lake, Rickie C. et al.
US 20010002826 A1	20010607	EMBEDDED CIRCUITS	343/872	343/870	TUTTLE, MARK E. et al.
US 20010002106 A1	20010531	Method of manufacturing and testing an electronic device, and an electronic device	324/754		Tuttle, Mark E. et al.
US 20010000299 A1	20010419	Electrical apparatuses, and methods of forming electrical apparatuses	340/573.1	340/384.2	Lake, Rickie C. et al.
US 7012504 B2	20060314	Wireless identification device, RFID device with push-on/push off switch, and method of manufacturing wireless identification device	340/10.1	340/10.34	Tuttle; Mark E.
US 6999339 B2	20060214	Integrated circuit including sensor to sense environmental data, method of compensating an MRAM integrated circuit for the effects of an external magnetic field, MRAM integrated circuit, and method of testing	365/158	365/211; 365/212; 365/213	Tuttle; Mark E. et al.
US 6962833 B2	20051108	Magnetic shield for integrated circuit packaging	438/55	257/E23.114; 257/E23.124; 438/127; 438/3	Tuttle; Mark E. et al.

US 6947513 B2	20050920	Radio frequency data communications device	375/374	340/10.1	O'Toole; James E. et al.
US 6941124 B1	20050906	Method of speeding power-up of an amplifier, and amplifier	455/341	330/261; 455/231	O'Toole; James E. et al.
US 6919613 B2	20050719	Magnetic tunneling junction antifuse device	257/530	257/295; 257/421; 257/E23.147	Seyyedy; Mirmajid et al.
US 6918965 B2	20050719	Single substrate annealing of magnetoresistive structure	148/108	438/3	Tuttle; Mark E. et al.
US 6906396 B2	20050614	Magnetic shield for integrated circuit packaging	257/433	257/659; 257/660; 257/704; 257/710; 257/729; 257/921; 257/E23.114; 257/E23.124; 438/3	Tuttle; Mark E. et al.
US 6903396 B2	20050607	Control of MTJ tunnel area	257/295	257/421; 438/3	Tuttle; Mark E.
US 6882284 B2	20050419	Electrical apparatuses, methods of forming electrical apparatuses, and termite sensing methods	340/693.5	29/825; 340/572.1; 340/573.1; 340/573.2; 340/652; 340/870.16; 424/405; 424/410; 424/84; 43/124; 43/132.1; 73/587; 73/865.8	Lake; Rickie C. et al.
US 6852550 B2	20050208	MRAM sense layer area control	438/3	216/22; 216/40; 360/324.2	Tuttle; Mark E. et al.
US 6836472 B2	20041228	Radio frequency data communications device	370/335	340/10.1; 340/10.33; 342/42; 342/44; 342/51;	O'Toole; James E. et al.

				708/250; 708/252; 713/322	
US 6836468 B1	20041228	Radio frequency data communications device	370/310	340/10.33; 342/42; 342/44; 342/51	O'Toole; James E. et al.
US 6825773 B1	20041130	Radio frequency data communications device	340/825.72	340/10.1; 342/42; 342/44; 342/51	O'Toole; James E. et al.
US 6806527 B2	20041019	Recessed magnetic storage element and method of formation	257/296		Tuttle; Mark E.
US 6797628 B2	20040928	Methods of forming integrated circuitry, semiconductor processing methods, and processing method of forming MRAM circuitry	438/694	257/E21.218; 257/E21.252; 257/E21.577; 438/702; 438/703; 438/725; 438/734	Li; Li et al.
US 6785159 B2	20040831	Combination etch stop and in situ resistor in a magnetoresistive memory and methods for fabricating same	365/158	257/E21.665; 365/171	Tuttle; Mark E.
US 6781508 B2	20040824	Radio frequency data communications device with adjustable receiver sensitivity and method	340/10.1	343/795	Tuttle; Mark E. et al.
US 6781173 B2	20040824	MRAM sense layer area control	257/295	257/429; 360/324.2; 365/158; 365/171; 365/173	Tuttle; Mark E. et al.
US 6774685 B2	20040810	Radio frequency data communications device	327/156		O'Toole; James E. et al.
US 6771613 B1	20040803	Radio frequency data communications device	370/277		O'Toole; James E. et al.
US 6770491 B2	20040803	Magnetoresistive memory and method of manufacturing the same	438/3		Tuttle; Mark E.

US 6768415 B1	20040727	Wireless identification device, RFID device with push-on/push-off switch, method of manufacturing wireless identification device	340/10.4		Tuttle; Mark E.
US 6751149 B2	20040615	Magnetic tunneling junction antifuse device	365/225.7	365/158; 365/171; 365/173; 365/66; 365/97	Seyyedy; Mirmajid et al.
US 6735183 B2	20040511	Radio frequency data communications device	370/311	340/10.33; 342/42; 342/44; 342/51	O'Toole; James E. et al.
US 6721289 B1	20040413	Radio frequency data communications device	370/311	340/10.33; 342/42; 342/44; 342/51	O'Toole; James E. et al.
US 6696879 B1	20040224	Radio frequency data communications device	327/356	327/359	O'Toole; James E. et al.
US 6693513 B2	20040217	Wireless identification device, RFID device with push-on/push off switch, and method of manufacturing wireless identification device	340/10.1	340/572.1; 340/572.3; 341/20	Tuttle; Mark E.
US 6600428 B1	20030729	Radio frequency data communications device	340/825.36		O'Toole; James E. et al.
US 6551852 B2	20030422	Method of forming a recessed magnetic storage element	438/73	438/3	Tuttle; Mark E.
US 6548207 B1	20030415	Thin-profile battery circuits and constructions, button-type battery circuits and constructions	429/158	429/157; 429/162	Tuttle; Mark E.
US 6545604 B1	20030408	Methods for electronic tracking of units originating from a common source, and assemblies comprising	340/572.1	340/572.9	Dando; Ross S. et al.

		transponders attached to meat spikes			
US RE37997 E	20030218	Polishing pad with controlled abrasion rate	451/548	451/287; 451/527; 451/550; 451/921	Tuttle; Mark E.
US 6515591 B2	20030204	Termite sensing methods	340/693.5	340/573.1; 340/573.2; 340/652; 340/870.16; 43/121; 43/124; 43/132.1	Lake; Rickie C. et al.
US 6509837 B1	20030121	Radio frequency data communications device with adjustable receiver sensitivity and method	340/572.7	340/10.1; 340/573.1; 343/795	Tuttle; Mark E. et al.
US 6509256 B2	20030121	Methods of forming electrically conductive interconnections and electrically interconnected substrates	438/618	228/115; 228/180.22; 257/E21.508; 257/E21.511; 438/108; 438/119; 438/612; 438/613; 438/614; 438/615	Medlen; Curtis M. et al.
US 6492192 B1	20021210	Method of making a Schottky diode in an integrated circuit	438/57	438/73	O'Toole; James E. et al.
US 6487681 B1	20021126	In-sheet transceiver testing	714/25	324/605; 324/613	Tuttle; Mark E. et al.
US 6478909 B1	20021112	Method for surface mounting electrical components to a substrate	156/85	156/273.7; 156/315; 257/E21.514; 29/832; 361/734; 361/746; 361/803	Tuttle; Mark E.
US 6466634 B1	20021015	Radio frequency data communications device	375/374	340/10.1	O'Toole; James E. et al.
US 6466131 B1	20021015	Radio frequency data communications	340/572.7	340/10.2; 343/795	Tuttle; Mark E. et al.

		device with adjustable receiver sensitivity and method			al.
US 6458234 B1	20021001	Methods of fixturing a flexible substrate and a processing carrier and methods of processing a flexible substrate	156/230	156/247; 156/249; 156/329; 156/344	Lake; Rickie C. et al.
US 6417561 B1	20020709	Keepers for MRAM electrodes	257/659	257/295; 257/390; 257/E21.665; 257/E27.005; 438/3	Tuttle; Mark E.
US 6413788 B1	20020702	Keepers for MRAM electrodes	438/3	257/326; 257/E21.665; 257/E27.005; 438/692	Tuttle; Mark E.
US 6392545 B2	20020521	Electrical apparatuses, and methods of forming electrical apparatuses	340/572.8	174/254; 174/259; 174/549; 29/825; 340/10.1; 340/573.1; 340/573.4; 340/870.16; 342/51; 361/600; 361/749; 361/772; 361/784; 361/792; 428/901; 43/132.1	Lake; Rickie C. et al.
US 6384648 B1	20020507	Radio frequency data communications device	327/156	331/57	O'Toole; James E. et al.
US 6380845 B2	20020430	Wireless identification device, RFID device, and method of manufacturing wireless identification device	340/10.4	340/10.34; 340/10.5; 340/5.61; 340/572.7; 342/44; 342/51	Tuttle; Mark E.
US 6375780 B1	20020423	Method of manufacturing an enclosed transceiver	156/226	156/227; 156/290; 340/572.1; 340/572.5;	Tuttle; Mark E. et al.

				340/572.8	
US 6373391 B1	20020416	Devices for sensing changes in environments proximate the devices, devices for sensing termites, and methods for sensing termites	340/573.1	43/132.1	Lake; Rickie C. et al.
US 6359561 B2	20020319	Method of manufacturing and testing an electronic device, and an electronic device	340/572.1	174/254; 29/846; 340/10.42; 340/572.4; 340/572.7; 340/572.8	Tuttle; Mark E. et al.
US 6357025 B1	20020312	Testing and burn-in of IC chips using radio frequency transmission	714/724	714/30; 714/733	Tuttle; Mark E.
US 6351190 B1	20020226	Stage having controlled variable resistance load circuit for use in voltage controlled ring oscillator	331/57	331/177R	O'Toole; James E. et al.
US 6344792 B1	20020205	Method of manufacturing and testing an electronic device, and a electronic device	340/10.42	174/254; 29/846; 361/749; 361/750; 361/751	Tuttle; Mark E. et al.
US 6339385 B1	20020115	Electronic communication devices, methods of forming electrical communication devices, and communication methods	340/825.71		Tuttle; Mark E.
US 6337634 B1	20020108	Radio frequency data communications device	340/825.56	340/10.5	O'Toole; James E. et al.
US 6329213 B1	20011211	Methods for forming integrated circuits within substrates	438/19	29/846; 438/126	Tuttle; Mark E. et al.
US 6325294 B1	20011204	Method of manufacturing an enclosed transceiver	235/492	235/380; 235/487	Tuttle; Mark E. et al.

US 6316975 B1	20011113	Radio frequency data communications device	327/156	327/159	O'Toole; James E. et al.
US 6314440 B1	20011106	Pseudo random number generator	708/250	708/252; 713/322	O'Toole; James E. et al.
US 6304185 B1	20011016	Devices for sensing living organisms, devices for sensing termites, methods of forming devices for sensing termites, and methods for sensing termites	340/573.2	340/10.1; 340/572.1; 340/573.1; 340/652; 340/870.16; 361/600; 361/748; 43/124; 43/132.1; 73/587; 73/865.8	Tuttle; Mark E. et al.
US 6281799 B1	20010828	Devices for sensing changes in environments proximate the devices, devices for sensing termites, and methods for sensing termites	340/573.1	340/870.16; 424/84; 43/124; 43/132.1	Lake; Rickie C. et al.
US 6278698 B1	20010821	Radio frequency data communications device	370/311	340/10.33	O'Toole; James E. et al.
US 6271801 B1	20010807	Embedded circuits	343/872	340/572.1; 368/82	Tuttle; Mark E. et al.
US 6271597 B1	20010807	Methods for forming electrically conductive interconnections and electrically interconnected substrates	257/774	257/777; 257/778; 257/783; 257/786; 257/E21.508; 257/E21.511	Medlen; Curtis M. et al.
US 6268078 B1	20010731	Thin-profile battery circuits and constructions, button-type battery circuits and constructions, methods of forming thin-profile battery circuits and constructions, and methods of forming	429/158	429/157; 429/162	Tuttle; Mark E.

		button-type battery circuits and constructions			
US 6255959 B1	20010703	Electrical apparatuses, methods of forming electrical apparatuses, and termite sensing methods	340/693.5	29/825; 340/572.1; 340/573.1; 340/573.2; 340/652; 340/870.16; 424/405; 424/410; 424/84; 43/124; 43/132.1; 73/587; 73/865.8	Lake; Rickie C. et al.
US 6249185 B1	20010619	Method of speeding power-up of an amplifier, and amplifier	330/261		O'Toole; James E. et al.
US 6243014 B1	20010605	Electrical apparatuses, termite sensing apparatuses, and methods of forming electrical apparatuses	340/573.1	340/541; 340/572.1; 340/652; 424/405; 424/410; 424/84	Lake; Rickie C. et al.
US 6240625 B1	20010605	Methods of forming thin-profile battery circuits and constructions, and methods of forming button-type battery circuits and constructions	29/623.4	29/623.1; 429/158; 429/162	Tuttle; Mark E.
US 6232677 B1	20010515	Apparatus and method of resetting an electric device	307/125	307/150	Tuttle; Mark E. et al.
US 6220516 B1	20010424	Method of manufacturing an enclosed transceiver	235/492	235/380	Tuttle; Mark E. et al.
US 6211785 B1	20010403	Method of manufacturing and testing an electronic device, and an electronic device	340/572.1	174/254; 29/846; 340/10.42; 340/572.4; 340/572.7; 340/572.8	Tuttle; Mark E. et al.

US 6208524 B1	20010327	Electronic apparatus, battery powerable apparatus, and radio frequency communication device	361/782	174/260; 174/521; 257/692; 257/787; 361/743; 361/760; 361/761; 361/763; 361/767; 361/768	Tuttle; Mark E.
US 6198357 B1	20010306	Stage having controlled variable resistance load circuit for use in voltage controlled ring oscillator	331/57	331/177R	O'Toole; James E. et al.
US 6198332 B1	20010306	Frequency doubler and method of doubling frequency	327/356	327/119; 327/122; 327/359	O'Toole; James E. et al.
US 6177859 B1	20010123	Radio frequency communication apparatus and methods of forming a radio frequency communication apparatus	340/10.1	235/492; 340/5.1; 340/572.1; 340/572.5; 342/22; 342/44; 342/50; 342/51	Tuttle; John R. et al.
US 6167614 B1	20010102	Method of manufacturing and testing an electronic device, and an electronic device	29/832	29/623.1; 29/623.5; 429/90	Tuttle; Mark E. et al.
US 6166915 A	20001226	Electronic circuits and circuit boards	361/748	174/256; 174/258; 174/260; 257/E23.075; 257/E23.125; 29/830; 29/846; 361/750; 361/760; 361/793; 428/901	Lake; Rickie C. et al.
US 6161205 A	20001212	Testing and burn-in of IC chips using radio frequency	714/724	714/30; 714/733	Tuttle; Mark E.

		transmission			
US 6157230 A	20001205	Method for realizing an improved radio frequency detector for use in a radio frequency identification device, frequency lock loop, timing oscillator, method of constructing a frequency lock loop and method of operating an integrated circuit	327/156	327/159; 331/57	O'Toole; James E. et al.
US 6143639 A	20001107	Methods of forming electrically conductive interconnections and electrically interconnected substrates	438/612	228/115; 228/3.1; 228/4.1; 257/737; 257/781; 257/784; 257/785; 257/E21.508; 257/E21.511; 257/E21.514; 438/107; 438/108; 438/119	Medlen; Curtis M. et al.
US 6130602 A	20001010	Radio frequency data communications device	340/10.33	342/42; 342/44; 342/51	O'Toole; James E. et al.
US 6114239 A	20000905	Electronic circuit bonding interconnect component and flip chip interconnect bond	438/653	257/E21.508; 257/E21.511; 257/E21.514; 257/E23.021; 438/108; 438/611; 438/613	Lake; Rickie C. et al.
US 6114060 A	20000905	Thin-profile battery circuits and constructions, button-type battery circuits and constructions, methods of forming thin-profile battery circuits and constructions, and	429/157	429/149; 429/156; 429/158; 429/162; 429/178	Tuttle; Mark E.

		methods of forming button-type battery circuits and constructions			
US 6110760 A	20000829	Methods of forming electrically conductive interconnections and electrically interconnected substrates	438/107	228/115; 228/179.1; 228/3.1; 228/4.1; 257/778; 257/779; 257/780; 257/781; 257/785; 257/E21.508; 257/E21.511; 438/108; 438/119; 438/612; 438/613; 438/614; 438/615	Medlen; Curtis M. et al.
US 6104280 A	20000815	Method of manufacturing and testing an electronic device, and an electronic device	340/10.42	174/254; 29/846; 361/749; 361/750; 361/751	Tuttle; Mark E. et al.
US 6081047 A	20000627	Apparatus and method of resetting an electric device	307/125	307/150	Tuttle; Mark E. et al.
US 6080509 A	20000627	Thin-profile battery circuits and constructions, button-type battery circuits and constructions, methods of forming thin-profile battery circuits and constructions, and methods of forming button-type battery circuits and constructions	429/157	429/158; 429/162	Tuttle; Mark E.
US 6074926 A	20000613	Method of increasing capacitance by surface roughening in semiconductor wafer	438/398	257/E21.013; 257/E21.396; 438/386	Cathey; David A. et al.

		<u>processing</u>			
US 6068947 A	20000530	Thin-profile battery circuits and constructions and button-type battery circuits and constructions	429/158	429/157; 429/162	Tuttle; Mark E.
US 6058497 A	20000502	Testing and burn-in of IC chips using radio frequency transmission	714/733	714/30	Tuttle; Mark E.
US 6052062 A	20000418	Cards, communication devices, and methods of forming and encoding visibly perceptible information on the same	340/5.64	257/787; 340/10.1; 340/10.3; 340/693.5	Tuttle; Mark E.
US 6048420 A	20000411	Method for surface mounting electrical components to a substrate	156/315	156/273.7; 156/85; 257/E21.514; 29/832	Tuttle; Mark E.
US 6045652 A	20000404	Method of manufacturing an enclosed transceiver	156/292	156/278; 340/10.1; 340/572.7; 343/908; 438/115; 438/125; 438/126; 438/127	Tuttle; Mark E. et al.
US 6037879 A	20000314	Wireless identification device, RFID device, and method of manufacturing wireless identification device	340/10.4	340/10.1; 340/571; 342/51	Tuttle; Mark E.
US 6027829 A	20000222	Insulative sealing gaskets and a thin profile battery	429/157	429/162; 429/171; 429/172; 429/173; 429/174; 429/185; 49/490.1	Tuttle; Mark E.
US 6025262 A	20000215	Method of passivating semiconductor wafers	438/624	257/E21.502; 257/E23.002; 257/E23.118;	Tuttle; Mark E. et al.

				257/E23.132; 438/633; 438/692; 438/763; 438/958; 438/959	
US 5986569 A	19991116	Radio frequency identification system, radio frequency identification device package, and method of use of radio frequency identification device	340/10.42	235/375; 343/702; 343/848	Mish; William et al.
US 5983363 A	19991109	In-sheet transceiver testing	714/25	324/605	Tuttle; Mark E. et al.
US 5973263 A	19991026	Encapsulated electronic component and method for encapsulating an electronic component	174/523	174/565; 257/790; 257/E21.502; 257/E23.119; 257/E23.126	Tuttle; Mark E. et al.
US 5972152 A	19991026	Methods of fixturing flexible circuit substrates and a processing carrier, processing a flexible circuit and processing a flexible circuit substrate relative to a processing carrier	156/247	156/249; 174/254; 264/272.11	Lake; Rickie C. et al.
US 5963177 A	19991005	Methods of enhancing electromagnetic radiation properties of encapsulated circuit, and related devices	343/872	340/10.1; 340/505; 343/741; 343/866	Tuttle; Mark E. et al.
US 5952121 A	19990914	Button-type battery with improved separator and gasket construction	429/162	429/174; 429/185	Blonsky; Peter M. et al.
US 5937512 A	19990817	Method of forming a circuit board	29/832	156/233; 257/E23.075; 257/E23.125; 29/840; 29/848	Lake; Rickie C. et al.
US 5919274	19990706	Method of forming a	29/623.2	29/623.1	Tuttle;

A		thin profile battery			Mark E.
US 5907477 A	19990525	Substrate assembly including a compartmental dam for use in the manufacturing of an enclosed electrical circuit using an encapsulant	361/760	174/260; 174/525; 235/491; 257/679; 257/787; 257/E23.14; 257/E23.177; 29/841; 343/702; 361/749	Tuttle; Mark E. et al.
US 5905296 A	19990518	Resistive structure for integrated circuits	257/536	257/379; 257/537; 257/538; 257/577; 257/904; 257/E27.047; 257/E27.051; 257/E27.101; 365/154	Tuttle; Mark E.
US 5893207 A	19990413	Method of forming a thin-profile battery	29/623.5	29/623.2; 427/250	Tuttle; Mark E.
US 5866277 A	19990202	Button type battery with improved separator and gasket construction	429/174	429/164; 429/175	Blonsky; Peter M. et al.
US 5851244 A	19981222	methods of forming thin profile batteries and methods of providing sealing gaskets between battery terminal housing members	29/623.2	29/623.1; 29/623.4; 429/162	Tuttle; Mark E.
US 5849632 A	19981215	Method of passivating semiconductor wafers	438/633	257/E21.502; 257/E23.002; 257/E23.118; 257/E23.132; 438/763; 438/958	Tuttle; Mark E. et al.
US 5849044 A	19981215	Method of forming thin profile batteries	29/623.2	29/623.1	Tuttle; Mark E.
US 5843596 A	19981201	Methods of forming button type batteries and to button type battery insulating and sealing gaskets	429/185	429/172; 429/173; 429/174	Tuttle; Mark E.

US 5820716 A	19981013	Method for surface mounting electrical components to a substrate	156/85	156/273.7; 156/315; 257/E21.514; 29/832	Tuttle; Mark E.
US 5807644 A	19980915	Thin profile battery	429/162	429/174; 429/185	Blonsky; Peter M. et al.
US 5804876 A	19980908	Electronic circuit bonding interconnect component and flip chip interconnect bond	257/737	257/750; 257/778; 257/E21.511; 257/E21.514; 257/E23.021	Lake; Rickie C. et al.
US 5800944 A	19980901	Thin profile battery with improved separator and gasket construction	429/174	429/162; 429/185	Blonsky; Peter M. et al.
US 5800943 A	19980901	Thin profile battery with improved separator and gasket construction	429/174	429/162; 429/185	Blonsky; Peter M. et al.
US 5800865 A	19980901	Thin profile battery with improved separator and gasket construction	429/174	429/162; 429/185	Blonsky; Peter M. et al.
US 5789104 A	19980804	Button-type battery with improved separator and gasket construction	429/164	429/162; 429/175	Blonsky; Peter M. et al.
US 5779839 A	19980714	Method of manufacturing an enclosed transceiver	156/213	156/308.4; 29/855	Tuttle; Mark E. et al.
US 5776278 A	19980707	Method of manufacturing an enclosed transceiver	156/213	156/308.4; 29/855	Tuttle; Mark E. et al.
US 5739577 A	19980414	Resistive structure for integrated circuits and method of forming same	257/536	257/379; 257/537; 257/538; 257/577; 257/904; 257/E21.004; 257/E27.047; 257/E27.051; 257/E27.101; 365/154	Tuttle; Mark E.
US 5725967 A	19980310	Battery container and method of	429/185	429/171; 429/172;	Tuttle; Mark E.

		manufacture		429/173; 429/174	
US 5724720 A	19980310	Methods of forming lithium electrodes	29/623.5	427/272; 427/282	Tuttle; Mark E.
US D388362 S	19971230	Personal elevated agricultural bed	D11/155		Tuttle; Mark E.
US 5700981 A	19971223	Encapsulated electronic component and method for encapsulating an electronic component	174/250	174/523; 257/787; 257/E21.502; 257/E23.119; 257/E23.126; 29/841; 361/765	Tuttle; Mark E. et al.
US D384908 S	19971014	Elevated agricultural bed	D11/155	D11/143	Tuttle; Mark E.
US 5676709 A	19971014	Method of forming battery terminal housing members and battery terminal housing member sheets	29/623.1	29/623.5	Tuttle; Mark E.
US 5665489 A	19970909	Thin profile batteries with solid electrolyte	429/172	429/171	Tuttle; Mark E.
US 5663598 A	19970902	Electrical circuit bonding interconnect component and flip chip interconnect bond	257/737	257/778; 257/E21.511; 257/E21.514; 257/E23.021	Lake; Rickie C. et al.
US 5663014 A	19970902	Thin profile battery with improved separator and gasket construction	429/175	429/164	Blonsky; Peter M. et al.
US 5662718 A	19970902	Method of forming thin profile batteries and a thin profile battery insulating and sealing gasket	29/623.1	429/171	Tuttle; Mark E.
US 5654110 A	19970805	Thin profile batteries and methods of forming the same	429/66	29/623.2; 429/162; 429/174; 429/176	Tuttle; John R. et al.
US 5652070 A	19970729	Thin profile battery	429/162	429/166; 429/171; 429/185	Blonsky; Peter M. et al.
US 5642562 A	19970701	Method of forming button-type battery lithium electrodes with	29/623.3	29/623.1	Tuttle; Mark E.

		housing member			
US 5642561 A	19970701	Methods of producing thin profile batteries and battery terminal housing members	29/623.2	429/174	Tuttle; Mark E. et al.
US 5633122 A	19970527	Test fixture and method for producing a test fixture for testing unpackaged semiconductor die	430/317	324/765; 430/311	Tuttle; Mark E.
US 5621913 A	19970415	System with chip to chip communication	455/41.2	327/564; 455/74; 455/90.2	Tuttle; Mark E. et al.
US 5612513 A	19970318	Article and method of manufacturing an enclosed electrical circuit using an encapsulant	174/260	257/782; 257/787; 257/E21.705; 257/E23.14; 257/E25.012; 29/832; 29/841; 361/765	Tuttle; Mark E. et al.
US 5601941 A	19970211	Improved battery assembly	429/121	429/162; 429/178	Tuttle; Mark E.
US RE35420 E	19970107	Method of increasing capacitance by surface roughening in semiconductor wafer processing	438/398	257/E21.013; 257/E21.396; 438/694; 438/695; 438/703; 438/964	Cathey; David A. et al.
US 5591680 A	19970107	Formation methods of opaque or translucent films	438/29	204/192.23; 204/192.25; 257/E31.122; 349/124; 349/138; 438/784	Tuttle; Mark E. et al.
US 5584891 A	19961217	Methods of forming a button-type battery terminal housing member sheet and of forming button-type batteries	29/623.1	29/623.5	Tuttle; Mark E.
US 5580674 A	19961203	Method of producing button-type batteries and spring-biased concave button-type battery	429/66	429/174; 429/176	Tuttle; John R. et al.

US 5558679 A	19960924	Method for mounting a battery on a substrate	29/623.1	29/623.5	Tuttle; Mark E.
US 5547781 A	19960820	Button-type battery with improved separator and gasket construction	429/174	429/175	Blonsky; Peter M. et al.
US 5542959 A	19960806	Method of forming button-type battery lithium electrodes	29/623.3		Tuttle; Mark E.
US 5494495 A	19960227	Method of forming button-type batteries	29/623.2	429/174	Tuttle; Mark E.
US 5486431 A	19960123	Method of producing button-type batteries and spring-biased concave button-type battery	429/66	29/623.2; 429/175; 429/176	Tuttle; John R. et al.
US 5480834 A	19960102	Process of manufacturing an electrical bonding interconnect having a metal bond pad portion and having a conductive epoxy portion comprising an oxide reducing agent	438/614	228/180.22; 257/E21.508; 257/E21.511; 257/E21.514; 257/E23.021; 438/119	Lake; Rickie C. et al.
US 5480462 A	19960102	Method of forming button-type battery lithium electrodes	29/623.5	427/272; 427/282	Tuttle; Mark E.
US 5432027 A	19950711	Button-type battery having bendable construction, and angled button-type battery	429/127	429/174	Tuttle; Mark E. et al.
US D359930 S	19950704	Elevated agricultural bed	D11/155		Tuttle; Mark E.
US 5421769 A	19950606	Apparatus for planarizing semiconductor wafers, and a polishing pad for a planarization apparatus	451/287	257/E21.23; 451/285; 451/288; 451/41; 451/526	Schultz; Laurence D. et al.
US 5384284 A	19950124	Method to form a low resistant bond pad interconnect	438/610	257/737; 257/915; 257/E21.582; 257/E23.02; 438/612;	Doan; Trung T. et al.

				438/678; 438/680	
US 5297364 A	19940329	Polishing pad with controlled abrasion rate	451/527	451/921	Tuttle; Mark E.
US 5275715 A	19940104	Electroplating process for enhancing the conformality of titanium and titanium nitride films in the manufacture of integrated circuits and structures produced thereby	205/123	205/157; 205/186; 205/224; 257/E21.175; 257/E21.29; 257/E21.582; 257/E21.584; 438/647; 438/648; 438/649; 438/658; 438/660	Tuttle; Mark E.
US 5244842 A	19930914	Method of increasing capacitance by surface roughening in semiconductor wafer processing	438/398	257/E21.013; 257/E21.396; 438/964	Cathey; David A. et al.
US 5234867 A	19930810	Method for planarizing semiconductor wafers with a non-circular polishing pad	438/692	257/E21.23	Schultz; Laurence D. et al.
US 5232875 A	19930803	Method and apparatus for improving planarity of chemical-mechanical planarization operations	438/693	216/88; 216/89; 438/959; 451/270; 451/63	Tuttle; Mark E. et al.
US 5202278 A	19930413	Method of forming a capacitor in semiconductor wafer processing	438/398	257/E21.012; 438/964	Mathews; Viju K. et al.
US 5177908 A	19930112	Polishing pad	451/41	451/287; 451/527; 451/548; 451/550; 451/921; D15/122	Tuttle; Mark E.
US 5151168 A	19920929	Process for metallizing integrated circuits with electrolytically-deposited copper	205/123	205/135; 257/E21.175; 257/E21.586; 257/E21.589;	Gilton; Terry L. et al.

				257/E21.597	
US 5142438 A	19920825	Dram cell having a stacked capacitor with a tantalum lower plate, a tantalum oxide dielectric layer, and a silicide buried contact	361/313	257/304; 257/310; 257/E21.648; 257/E27.086; 438/240; 438/396	Reinberg; Alan R. et al.
US 5124780 A	19920623	Conductive contact plug and a method of forming a conductive contact plug in an integrated circuit using laser planarization	257/344	219/121.66; 257/437; 257/E21.303; 257/E21.583; 257/E21.586; 257/E23.16; 427/555; 438/642; 438/660; 438/672; 438/799	Sandhu; Gurtej S. et al.
US 5112773 A	19920512	Methods for texturizing polysilicon utilizing gas phase nucleation	438/398	257/E21.013; 257/E21.297; 257/E21.538; 438/665; 438/684; 438/964	Tuttle; Mark E.
US 5102832 A	19920407	Methods for texturizing polysilicon	438/398	257/E21.013; 257/E21.297; 257/E21.648; 427/212; 438/665; 438/684; 438/964	Tuttle; Mark E.
US 5073518 A	19911217	Process to mechanically and plastically deform solid ductile metal to fill contacts of conductive channels with ductile metal and process for dry polishing excess metal from a semiconductor wafer	438/618	257/E21.304; 257/E21.577; 257/E21.583; 257/E21.588; 438/662; 438/672; 438/959	Doan; Trung et al.
US 5069747 A	19911203	Creation and removal of temporary silicon dioxide structures on an in-process	438/696	257/E21.038; 257/E21.279; 257/E21.346; 438/303;	Cathey; David A. et al.

		integrated circuit with minimal effect on exposed, permanent silicon dioxide structures		438/595; 438/701	
US 5021353 A	19910604	Split-polysilicon CMOS process incorporating self-aligned silicidation of conductive regions	438/231	257/369; 257/408; 257/E21.438; 257/E21.636; 438/227	Lowrey; Tyler A. et al.
US 5020283 A	19910604	Polishing pad with uniform abrasion	451/550	451/527; 451/548; D15/122	Tuttle; Mark E.
US 4851127 A	19890725	Novel amine-based precursor compounds and composite membranes thereof	210/654	210/490; 210/500.37; 564/142	Lee; Eric K. L. et al.
US 4622344 A	19861111	Recovery of ammoniacal copper with novel organogels	521/28	521/27; 524/167; 524/210; 524/357; 524/87	Babcock; Walter C. et al.